

1.8V, μPower, Auto-Zero, Rail-toRail I/O Amplifier

Preliminary

ADA4051-2

FEATURES

Very Low Supply Current: 15 μA/amp (Max) 10μV Offset Voltage (Max) 50nV/°C Voltage Offset Drift (Max) 1.8V to 5.5V Single Supply Operation Rail-to-Rail Input and Output High PSRR and CMRR: 106 dB min

APPLICATIONS

Pressure and Position Sensors Temperature Measurements Electronic Scales Medical Instrumentation Battery Powered Equipment Handheld Test Equipment

GENERAL DESCRIPTION

The ADA4051-2 is a dual CMOS high precision operational amplifier featuring rail-to-rail input and output swings, micropower, and extremely low offset voltage while operating from a 1.8 V to 5.5 V single power supply.

Employing a new circuit technology, these low cost amplifiers offer high PSRR and CMRR, while operating with a supply current of 15 μ A per amplifier maximum.

This combination of features makes the ADA4051 amplifier an ideal choice for battery powered applications where it is important to minimize power consumption and the need for high precision op amps.

The ADA4051-2 is specified for the extended industrial $(-40^{\circ} \text{ to } +125^{\circ}\text{C})$ temperature range, but it is operational from $-40^{\circ} \text{ to } +150^{\circ}\text{C}$. ADA4051-2 dual amplifier is available in the standard 8-pin MSOP and 8-pin LFCSP packages.





PrelimRev PrB

One Technology Way, PO Box 9106, Norwood, MA 02062-9106, USA Tel: 617/329-4700 World Wide Web Site:http://www.analog.com Fax: 617/326-8703 © Analog Devices, Inc., 2009

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices.

Preliminary Technical Data **ELECTRICAL CHARACTERISTICS**

ADA4051-2

(@ $V_S = +1.8V$ to +5V, $V_{CM} = V_S/2$, $T_A = +25^{\circ}C$, $R_L = 10k\Omega$ to $V_S/2$, $V_{OUT} = V_S/2$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Тур	Max	Units
INPUT CHARACTERISTICS						
Offset Voltage	V _{os}	$0v < V_{CM} < 5V$		2	10	μV
Offset Voltage Drift	$\Delta V_{OS} / \Delta T$	$-40^{\circ} < T_A < +125^{\circ}C$		0.02	0.05	μV/°C
Input Bias Current	I _B	$-40^{\circ} < T_A < +125^{\circ}C$		±70 ±150	±200	pA pA
Input Offset Current	I _{OS}			±140	±400	pА
Input Common Mode Voltage Range	V _{CM}		(V-) – 0.1		(V+) + 0.1	v
Common-Mode Rejection Ratio	CMRR	$(v_{-}) - 0.1v < v_{CM} < (v_{+}) + 0.1v$ $-40^{\circ} < T_A < +125^{\circ}C$	106	130		dB
Channel Separation		DC		0.1		μV/V
Open Loop Voltage Gain	A _{OL}	$(V-) - 0.1V < V_{CM} < (V+) + 0.1V$	106	130		dB
Input Capacitance		Differential Common Mode		2 4		pF pF
OUTPUT CHARACTERISTICS						
Output Voltage Swing from Rail		$R_{\rm L} = 10 \ {\rm k}\Omega$ to $V_{\rm S}/2$		30	50	mV
		$-40^{\circ} < T_A < +125^{\circ}C$			70	mV
Short Circuit Limit	I _{SC}			±25		mA
Open Loop Output Impedance		$f = 350 \text{kHz}, I_0 = 0$		2		kΩ
POWER SUPPLY Power Supply Rejection Ratio	PSRR	$V_{S} = 1.8V$ to 5.5V -40°C to +125°C	106	120		dB
Supply Current/Amplifier	I _{SY}	$V_{O} = V_{S}/2, I_{O} = 0$		12	15 20	μA
Supply Voltage	V _{SY}	-40 < 1A < +125 C	1.8		5.5	V
Turn-On Time	t _{ON}	$V_{S} = 5V$		100		μs
DYNAMIC PERFORMANCE						
Slew Rate	SR+	G = +1		0.035		V/µs
Gain Bandwidth Product	GBP	$C_L = 100 pF$		100		ν/μs kHz
NOISE PERFORMANCE						
Input Voltage Noise	e _{n p-p}	f = 0.01Hz to 1Hz f = 0.1Hz to 10Hz		TBD 1.9		μV _{p-p} μV _{p-p}
Voltage Noise Density	en	f=10Hz		95		nV/vHz
Current Noise Density	i _n	f=10Hz		100		fA/√Hz
TEMPERATURE RANGE						
Specified Range	Ts		-40		+125	°C
Operating Range	T _A		-40		+150	°C

Preliminary Technical Data

ADA4051-2

ABSOLUTE MAXIMUM RATINGS ^1 $\,$

Supply voltage	+6V
Input Voltage	±Vs
Differential Input Voltage ¹	±Vs
Output Short-Circuit Duration to Gnd	Indefinite
Storage Temperature Range	
KS, RJ, CP, RM Packages	65°C to +150°C
Operating Temperature Range	
ADA405-1/2	40°C to +125°C
Junction Temperature Range	
KS, RJ, CP, RM Packages	65°C to +150°C
Lead Temperature Range (Soldering, 60 Sec)	+260°C

Package Type	θJA^2	θJC	Units
8-Pin MSOP (RM)	TBD	TBD	°C/W
8-bump LFCSP (CP)	TBD	TBD	°C/W

NOTES

 1 Differential input voltage is limited to 5V or the supply voltage whichever is less.

 2 θ_{JA} is specified for the worst case conditions, i.e., θ_{JA} is specified for device soldered in circuit board for surface mount packages.

ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option
ADA4051-2ARMZ	-40°C to +125°C	8-Pin MSOP	RM-8
ADA4051-2ACPZ	-40°C to +125°C	8-Pin LFCSP	CP-8

ESD Caution

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulates on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



©2009 Analog Devices, Inc. All rights reserved. Trademarks and registered trademarks are the property of their respective owners. PR08056-0-5/09(PrA)



www.analog.com